TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

### TC74AC157P,TC74AC157F,TC74AC157FT

#### **Quad 2-Channel Multiplexer**

The TC74AC157 is an advanced high speed CMOS QUAD 2-CHANNEL MULTIPLEXER fabricated with silicon gate and double-layer metal wiring  $C^2MOS$  technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

This device consist of four 2-input digital multiplexer with common select and strobe inputs.

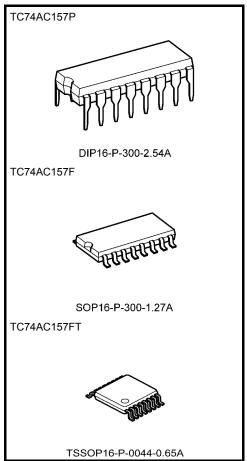
When the STROBE input is held "H" level, selection of data is inhibited and all the outputs become "L" level.

The SELECT decoding determines whether the A or B inputs get routed to their corresponding Y outputs.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

#### **Features**

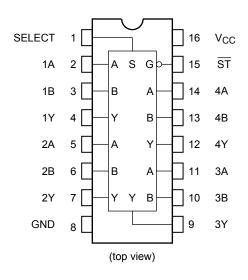
- High speed:  $t_{pd} = 4.5 \text{ ns}$  (typ.) at  $V_{CC} = 5 \text{ V}$
- Low power dissipation:  $I_{CC} = 8 \mu A \text{ (max)}$  at  $T_{a} = 25 \text{°C}$
- High noise immunity:  $V_{NIH} = V_{NIL} = 28\% V_{CC}$  (min)
- Symmetrical output impedance:  $|I_{OH}| = I_{OL} = 24$  mA (min) Capability of driving 50  $\Omega$  transmission lines.
- $\bullet \quad \text{Balanced propagation delays: } t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range:  $V_{CC (opr)} = 2 \text{ to } 5.5 \text{ V}$
- Pin and function compatible with 74F157



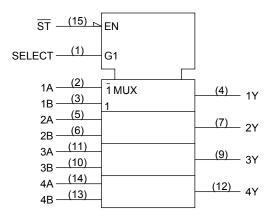
Weight

DIP16-P-300-2.54A : 1.00 g (typ.) SOP16-P-300-1.27A : 0.18 g (typ.) TSSOP16-P-0044-0.65A : 0.06 g (typ.)

# **Pin Assignment**



# **IEC Logic Symbol**

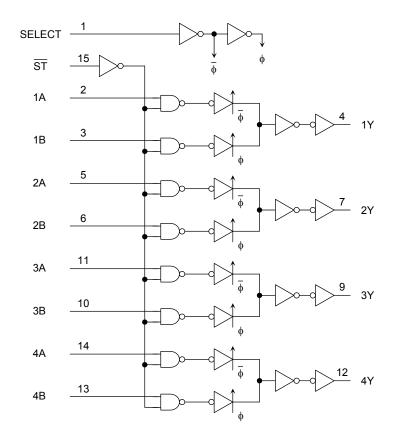


**Truth Table** 

	Inputs	Output		
ST	SELECT	Α	В	Y
Н	Х	Х	Х	L
L	L	L	Х	L
L	L	Н	Х	Н
L	Н	Х	L	L
L	Н	Х	Н	Н

X: Don't care

### **System Diagram**



### **Absolute Maximum Ratings (Note 1)**

Characteristics	Symbol	Rating	Unit
Supply voltage range	V <sub>CC</sub>	−0.5 to 7.0	V
DC input voltage	V <sub>IN</sub>	-0.5 to V <sub>CC</sub> + 0.5	V
DC output voltage	V <sub>OUT</sub>	−0.5 to V <sub>CC</sub> + 0.5	٧
Input diode current	I <sub>IK</sub>	±20	mA
Output diode current	lok	±50	mA
DC output current	lout	±50	mA
DC V <sub>CC</sub> /ground current	Icc	±100	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP/TSSOP)	mW
Storage temperature	T <sub>stg</sub>	−65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C should be applied up to 300 mW.

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# **Operating Ranges (Note)**

Characteristics	Symbol	Rating	Unit	
Supply voltage	V <sub>CC</sub>	2.0 to 5.5	V	
Input voltage	V <sub>IN</sub>	0 to V <sub>CC</sub>	V	
Output voltage	V <sub>OUT</sub>	0 to V <sub>CC</sub>	V	
Operating temperature	T <sub>opr</sub>	−40 to 85	°C	
Input rise and fall time	dt/dV	0 to 100 (V <sub>CC</sub> = 3.3 ± 0.3 V)	ns/V	
input rise and fail time	αναν	0 to 20 ( $V_{CC} = 5 \pm 0.5 \text{ V}$ )		

Note: The operating ranges must be maintained to ensure the normal operation of the device.
Unused inputs must be tied to either VCC or GND.

### **Electrical Characteristics**

### **DC Characteristics**

Characteristics	Symbol	Test Condition				Ta = 25°C		Ta = -40 to 85°C		Unit	
Characteriotics	Cymbol			V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Onne	
	V <sub>IH</sub>	_		2.0	1.50	_	_	1.50	_	V	
High-level input voltage				3.0	2.10	_	_	2.10	_		
<u> </u>				5.5	3.85	_	_	3.85	_		
					2.0	_	_	0.50	_	0.50	
Low-level input voltage	$V_{IL}$	_		3.0	_	_	0.90	_	0.90	V	
				5.5	_	_	1.65	_	1.65		
	V <sub>ОН</sub>	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> = -50 μA		2.0	1.9	2.0	_	1.9	_	- v
					3.0	2.9	3.0	_	2.9	_	
High-level output					4.5	4.4	4.5	_	4.4	_	
voltage			$I_{OH} = -4 \text{ mA}$		3.0	2.58	_	_	2.48	_	
			I <sub>OH</sub> = -24 mA		4.5	3.94	_	_	3.80	_	
			I <sub>OH</sub> = -75 mA	(Note)	5.5	_	_	_	3.85	_	
	VoL	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>			2.0	_	0.0	0.1	_	0.1	
			I <sub>OL</sub> = 50 μA		3.0	_	0.0	0.1	_	0.1	
Low-level output					4.5	_	0.0	0.1	_	0.1	V
voltage			I <sub>OL</sub> = 12 mA		3.0	_	_	0.36	_	0.44	·
			I <sub>OL</sub> = 24 mA		4.5	_	_	0.36	_	0.44	
			I <sub>OL</sub> = 75 mA	(Note)	5.5	_	_	_	_	1.65	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		5.5	I	_	±0.1	_	±1.0	μΑ	
Quiescent supply current	Icc	V <sub>IN</sub> = V <sub>CC</sub> or GND		5.5	_	_	8.0	_	80.0	μA	

Note: This spec indicates the capability of driving 50  $\Omega$  transmission lines.

One output should be tested at a time for a 10 ms maximum duration.



### AC Characteristics (C<sub>L</sub> = 50 pF, R<sub>L</sub> = 500 $\Omega$ , input: $t_r$ = $t_f$ = 3 ns)

Characteristics	Symbol	Test Condition	ndition		Ta = 25°C			Ta = −40 to 85°C	
	- Cy		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time (A, B-Y)	t <sub>pLH</sub>	_	$3.3 \pm 0.3$ $5.0 \pm 0.5$	_ _	7.2 5.5	12.2 7.9	1.0 1.0	14.0 9.1	ns
Propagation delay time (SELECT-Y)	t <sub>pLH</sub>	_	$3.3 \pm 0.3$ $5.0 \pm 0.5$	_ _	8.5 6.3	14.5 9.1	1.0 1.0	16.7 10.5	ns
Propagation delay time (ST -Y)	t <sub>pLH</sub>	_	$3.3 \pm 0.3$ $5.0 \pm 0.5$		8.6 6.4	14.6 9.2	1.0 1.0	16.8 10.6	ns
Input capacitance	C <sub>IN</sub>	_	-	_	5	10	_	10	pF
Power dissipation capacitance	C <sub>PD</sub>		(Note)	_	93	_	_	_	pF

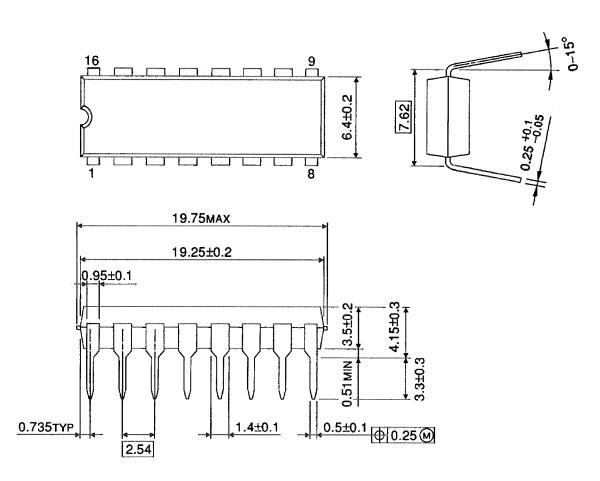
Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4 \text{ (per bit)}$ 

# **Package Dimensions**

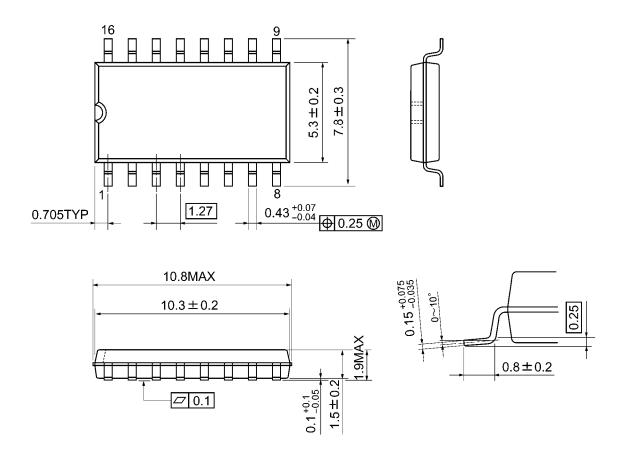
DIP16-P-300-2.54A Unit: mm



Weight: 1.00 g (typ.)

# **Package Dimensions**

SOP16-P-300-1.27A Unit: mm

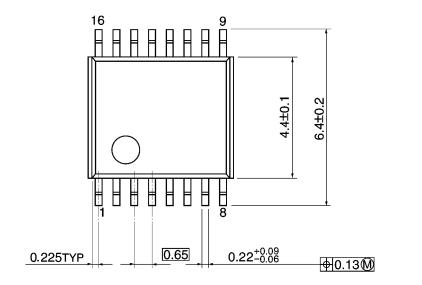


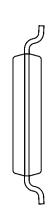
Weight: 0.18 g (typ.)

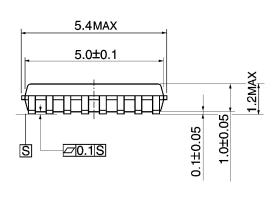
# **Package Dimensions**

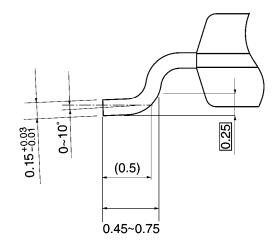
TSSOP16-P-0044-0.65A

Unit: mm









Weight: 0.06 g (typ.)

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